	114	711/167 and (((minimum maximum) near5 (latency delay)) and (memory	USPAT;	2004/08/24 11:05
•	114	near5 (module banks)))	US-PGPUB;	2004/08/24 11.03
		nears (module banks)))		
	1013	711/169	EPO; JPO	2004/07/23 11:56
-	1013	711/109	USPAT;	2004/07/23 11:36
	į		US-PGPUB;	
	2276	265104	EPO; JPO	2004/07/22 11.56
-	2376	365/194	USPAT;	2004/07/23 11:56
			US-PGPUB;	
	1405		EPO; JPO	2004/07/22 11 50
•	1405	(minimiz\$3 reduc\$3) near10 (delay near5 (access read write))	USPAT;	2004/07/23 11:58
			US-PGPUB;	
	2027	(:-:-:-::	EPO; JPO	2004/07/22 11 50
•	3927	(minimiz\$3 reduc\$3) same (delay near10 (access read write))	USPAT;	2004/07/23 11:58
			US-PGPUB;	
		(EPO; JPO	2004/05/20 12 00
•	65	(system near5 clock) same ((minimiz\$3 reduc\$3) same (delay near10	USPAT;	2004/07/23 12:08
		(access read write)))	US-PGPUB;	
			EPO; JPO	
-	44	(equaliz\$3 compesat\$3 stabiliz\$3 offset\$4 counterbalanc\$3) near10	USPAT;	2004/07/26 09:54
		((read write access) near6 latency)	US-PGPUB;	
			EPO; JPO	
-	96	(equaliz\$3 compensat\$3 stabiliz\$3 offset\$4 counterbalanc\$3) near10	USPAT;	2004/07/23 12:10
		((read write access) near6 latency)	US-PGPUB;	
			EPO; JPO	
-	172	minimum near8 (device memory) near9 latency	USPAT;	2004/07/23 12:11
			US-PGPUB;	
			ЕРО; ЈРО	
•	12	((equaliz\$3 compensat\$3 stabiliz\$3 offset\$4 counterbalanc\$3) near10	USPAT;	2004/07/23 12:11
		((read write access) near6 latency)) and (minimum near8 (device	US-PGPUB;	
		memory) near9 latency)	EPO; JPO	
•	1	6658523.pn.	USPAT;	2004/07/26 09:53
			US-PGPUB;	
			EPO; JPO	
-	2376	365/194	USPAT;	2004/07/26 09:54
			US-PGPUB;	
	1		EPO; JPO	
•	44	(equaliz\$3 compesat\$3 stabiliz\$3 offset\$4 counterbalanc\$3) near10	USPAT;	2004/07/26 10:33
		((read write access) near6 latency)	US-PGPUB;	
			EPO; JPO	
-	10	((equaliz\$3 compesat\$3 stabiliz\$3 offset\$4 counterbalanc\$3) near10	USPAT;	2004/07/26 09:55
		((read write access) near6 latency)) and 365/194	US-PGPUB;	
			EPO; JPO	
-	11	(equaliz\$3 leveliz\$3) near10 ((read write access) near6 latency)	USPAT;	2004/07/26 10:35
			US-PGPUB;	
			EPO; JPO	
-	30	(equaliz\$3 leveliz\$3) same ((read write access) near6 latencies)	USPAT;	2004/07/26 10:35
			US-PGPUB;	
			EPO; JPO	
•	8	(memory near10 devices) same ((equaliz\$3 leveliz\$3) same ((read write	USPAT;	2004/07/26 11:26
		access) near6 latencies))	US-PGPUB;	
			EPO; JPO	
-	0	"20030023815"	USPAT	2004/07/26 11:32
-	1	"20030023815"	US-PGPUB	2004/07/26 11:32

L Number	Hits	Search Text	DB	Time stamp
2	0	711/105 same "2"	USPAT;	2004/08/24 11:04
			US-PGPUB;	
			EPO; JPO	
3	63	minimum near4 read near10 latency	USPAT;	2004/08/24 11:03
		ĺ	US-PGPUB;	
			EPO; JPO	
4	9	711/105 and (minimum near4 read near10 latency)	USPAT;	2004/08/24 11:04
4	,	7117103 and (minimum near 110ad near 10 latertoj)	US-PGPUB;	200 11 00 12 1 11 10 1
			EPO; JPO	
5	10	711/167 and (minimum near4 read near10 latency)	USPAT;	2004/08/24 11:04
	10	/ 11/10/ and (minimum near4 read near10 fatency)	US-PGPUB;	2004/00/24 11:04
			EPO; JPO	
6	_	711/1/0 1 (i-i	1 '	2004/08/24 11:04
	5	711/169 and (minimum near4 read near10 latency)	USPAT;	2004/08/24 11:04
İ			US-PGPUB;	
_			EPO; JPO	
7	4	711/170 and (minimum near4 read near10 latency)	USPAT;	2004/08/24 11:04
i			US-PGPUB;	
			EPO; JPO	
8	10	365/194 and (minimum near4 read near10 latency)	USPAT;	2004/08/24 11:04
			US-PGPUB;	
			EPO; JPO	
9	91	365/194 and (((minimum maximum) near5 (latency delay)) and (memory	USPAT;	2004/08/24 11:05
		near5 (module banks)))	US-PGPUB;	
			EPO; JPO	
10	53	711/169 and (((minimum maximum) near5 (latency delay)) and (memory	USPAT;	2004/08/24 11:05
.	33	near5 (module banks)))	US-PGPUB:	2001/00/21 11:03
		Hears (Hoddie banks)))	EPO; JPO	
11	91	365/194 and (365/194 and (((minimum maximum) near5 (latency delay))	USPAT;	2004/08/24 11:05
	91			2004/06/24 11.03
		and (memory near5 (module banks))))	US-PGPUB;	
-	756	(EPO; JPO	2004/05/15 15 45
	756	(system cpu) near10 (read near5 (latency delay))	USPAT;	2004/07/15 15:47
			US-PGPUB;	
			EPO; JPO	
-	965	device near10 (read near7 (latency delay))	USPAT;	2004/07/15 15:31
			US-PGPUB;	
]			EPO; JPO	
-	67	((system cpu) near10 (read near5 (latency delay))) same (device near10	USPAT;	2004/07/15 15:23
		(read near7 (latency delay)))	US-PGPUB;	
			EPO; JPO	
-	8	sum near10 (device near4 read near4 latency)	USPAT;	2004/07/26 09:54
			US-PGPUB;	
			EPO; JPO	
	8	sum near10 (device near4 read near4 latenc\$3)	USPAT;	2004/07/15 15:29
	J		US-PGPUB;	
			EPO; JPO	
_	7	((system cpu) near10 (read near5 (latency delay))) and (sum near10	USPAT;	2004/07/15 15:29
-	,	((system cpu) hear to (read hears (fatchey defay))) and (sum hear to (device near4 read near4 latenc\$3))	US-PGPUB;	2007/01/13 13.29
		(ucvice near4 reau near4 rateneas))	EPO; JPO	
	7	((t)10 (d (leter d-le))) (10		2004/07/15 15:20
-	7	((system cpu) near10 (read near5 (latency delay))) same (sum near10	USPAT;	2004/07/15 15:29
		(device near4 read near4 latenc\$3))	US-PGPUB;	
			EPO; JPO	
-	11	(sum\$3 add\$3) near10 (device near4 read near4 latency)	USPAT;	2004/07/15 15:31
			US-PGPUB;	
			EPO; JPO	
-	24	(device near10 (read near7 (latency delay))) near10 maximum	USPAT;	2004/07/15 15:32
l			US-PGPUB;	
			EPO; JPO	
-	55	(minimum near5 read near3 (latency delay)) and (memory near5 (module	USPAT;	2004/07/15 15:47
	55	banks))	US-PGPUB;	2007/07/13 13.47
		vaika)j		
	1670	((minimum maximum) mags (1-to 1-1\) 1 (EPO; JPO	2004/03/15 15 10
-	1570	((minimum maximum) near5 (latency delay)) and (memory near5	USPAT;	2004/07/15 15:48
	•	(module banks))	US-PGPUB;	İ
		<u> </u>	EPO; JPO	